**Planning for the Electronics v2**

**Year 1**

***Day 0:***

* get access to money

***Day 1:***

* request update of all quotes

***Day 7:***

* Order: all electronics

***Day 120 – 180:***

- Receive delivery of electronics at PSI

- verify that all electronics are working

***Day 181 – 240:***

* Work out mounting / power / infrastructure questions in collaboration with PSI
* Mount readout as close to the “final” required positions as possible
* Work out necessary cable routing and lengths
* Ordering of necessary cable lengths and connectors

***Day 241-300:***

* Prototyping and manufacture of cables necessary for communication between detectors and electronics
* Testing of cables
* Installation of cabling

***Day 301 – 365***

* Installation of MIDAS software on VME controllers
* Test of the addressing and communication of all aspects of the electronics
* Work on integration of complete DAQ

**Year 2**

***Day 1 -7:***

* Continue work on integration of complete DAQ, ensuring synchronization
* Order the RAID array for data storage

***Day 120 – 270:***

* Install electronics in final position and complete cabling
* Installation of MIDAS software on remaining VME controllers
* Work on integration of complete DAQ

***Day 270 – 365***

* Final testing and improvement of the DAQ, ready for complete experiment.

***Issues to Consider***

1. ***CAEN electronics and GSI (PADIWA & TRB3s) electronics come from Europe, prices are highly dependent on the EUR / USD exchange rate***
2. ***We need to work on programming of FPGAs on trigger distribution units for VME systems***
3. ***Need to finalize the version of the PADIWAS required before ordering the final version from GSI. It may lead to a delay of approximately 3 months in delivery of the new PADIWAs allowing for new board layouts to be completed. This can be partially compensated by working on other aspects of the DAQ first.***
4. ***We need to establish the exact mechanism of delivering individual gates to the MQDC-32 channels in order to avoid masses of delay cable. Should this fail, the extra cost of the delay cable would be partly offset by not having to build the extra cabling splitter-to-splitter, the extra splitters and the cables between splitters and the MQDC gate inputs, but may increase the cost somewhat and would cause extra physical challenges in how we mount and manage the delay cables.***