Slow Hub Version 2

 Modifications From Fast Hub

 Divide By 64

 40 MHz Clk+T1

 Start-Stop (Enables All Hub Functions)

 Divide By 2

 Serial Data In

 8 Bit Reg.

 Address Compare

 Serial Data Return

 D-Latch (Odd Bits Only)

 1 to 7 Decoder

 1 to 7 Decoder

 7 to 1 Mux

 Slow Clock Out

 Slow Data I/O

 Rev 2